



## Q&A

### What are the pinout assignments for the IQ710 V.35 connector?

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**Q:** What are the pinout assignments for the IQ710 V.35 connector?

**A:** The pinout assignments for the IQ710 V.35 connector are listed in the following matrix.

Pin	CCITT	Description
A	101	Protective Ground (PG)
B	102	Signal Ground (SG)
C	105	Request to Send (RTS) from DTE
D	106	Clear to Send (CTS) to DTE
E	107	Data Set Ready (DSR) to DTE
F	109	Received Line Signal Detector (CD) to DTE
H	-	Data Terminal Ready (DTR) from DTE
J	-	Ring Indicator (RI)
R	104	Received Data (RD-A) to DTE
T	104	Received Data (RD-B) to DTE
V	115	RX Clock (RC-A) to DTE
X	115	RX Clock (RC-B) to DTE
P	103	Transmitted Data (TD-A) from DTE
S	103	Transmitted Data (TD-B) from DTE
Y	114	TX Clock (TC-A) to DTE
AA	114	TX Clock (TC-B) to DTE
U	113	External TX Clock (ETC-A) from DTE
W	113	External TX Clock (ETC-B) from DTE
NN	-	Test Mode (TM) to DTE

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