



## INSTALLATION INSTRUCTIONS

1. Remove the cover plate from the appropriate option slot in the T3SU 300 Base Unit.
2. Slide the T3SU V.35 DTE Interface Module into the option slot until the module is firmly positioned against the front of the chassis.
3. Secure the thumbscrews at both edges of the module. Tighten with a screwdriver.
4. Connect the cables to the associated device(s).
5. Complete installation of remaining modules and Base Unit as specified in the T3SU 300 User Manual.

## SPECIFICATIONS

<b>Interface Type:</b>	Winchester-style 34-pin V.35 connector (female)
<b>Data Rate:</b>	Supports rates up to 10.5 Mbps (in 75 kbps blocks)
<b>DTE Transmit Clock:</b>	Internal (slaved to the network receive clock) and external
<b>Environmental Specifications:</b>	Operating Temperature: 0°C to 50°C Storage Temperature: -20°C to 70°C Relative Humidity: Up to 95% non-condensing
<b>Compliance:</b>	FCC Part 15, Class A UL 60950, 3rd Edition

## NETWORK CONNECTION PINOUT

Pin #	Name	Description
A	101	Protective ground (PG)
B	102	Signal ground (SG)
C	105	Request to send (RTS) from DTE
D	106	Clear to send (CTS) to DTE
E	107	Data set ready (DSR) to DTE
F	109	Received line signal detector (DCD) to DTE
H	—	Data terminal ready (DTR) from DTE
J	—	Ring indicator (RI)
R	104	Received data (RD-A) to DTE
T	104	Received data (RD-B) to DTE
V	115	RX clock (RC-A) to DTE
X	115	RX clock (RC-B) to DTE
P	103	Transmitted data (TD-A) from DTE
S	103	Transmitted data (TD-B) from DTE
Y	114	TX clock (TC-A) to DTE
AA	114	TX clock (TC-B) to DTE
U	113	External TX clock (ETC-A) from DTE
W	113	External TX clock (ETC-B) from DTE
NN	—	Test mode (TM) to DTE

**MENU TREE**

1 - Status	1 - DS3 Network	Interface Type	V.35
2 - Statistics	2 - DTE Ports	Port Status	
3 - Configuration	3 - System Management	1 - Port State	
4 - Diagnostic	4 - Utilities	2 - Nx75k blocks (1-140)	
5 - Remote Login		Port Bandwidth	
6 - Logout		Unallocated Blocks	
		3 - Apply Settings	
		4 - CS	1 - Forced ON
			2 - Follow RS
		5 - TR	1 - Ignored
			2 - Idle when OFF
		6 - SR	1 - Forced ON
			2 - OFF when OOS/OOF
		7 - CD	3 - OFF when TEST
			4 - OFF when OOS/OOF or TEST
		8 - Transmit Clock	1 - Forced ON
			2 - OFF when OOS/OOF
			Normal
			Invert
			External