



INSTALLATION INSTRUCTIONS

1. Hold the plug-on board above the option module.
2. Using a downward and right to left motion, slip the V.35 connector plug into the opening in the option module back panel.
3. Moving the plug-on board downward, secure the connection of the header pins at the front of the boards.
4. Install the two 4-40 screws at both edges of the option module.

SPECIFICATIONS - Nx56/64 AND DBU

Mechanical	Compatible with option slot of all TSUs
Environmental	Operating temperature from 0 °C to 45 °C
Tests	Extensive self-tests

SPECIFICATIONS - DTE INTERFACE

DTE Interface	CCITT V.35 synchronous
Backup Delay	1 sec, 3 sec, 10 sec, 30 sec, 1 min, 5 min, 10 min
DBU Dialing	DTR dialing
Restore Delay	1 sec, 3 sec, 10 sec, 30 sec, 1 min, 5 min, 10 min, Never
Rates	56 kbps - 2.048 mbps in 56K or 64 K increments
Clocking	Routes external DCE clocks to external DTE when active
Connector	Winchester (V.35) female
Test	Loopback to DCE Test dialup network Force dial backup

SPECIFICATIONS - DCE OPTIONS

DCE Interface	CCITT V.35 synchronous
Rates	56 Kbps to 2.048 Mbps in 56K or 64K increments
Clock Options	Internal or internal-invert
Tests	Local loopback (bilateral) Remote loopback (V.34)
Test Pattern	511 with errored seconds display and error inject capability
Data Inversion	Menu selectable
1's Density Protection	Force 1s to network after 1 second of consecutive zeros for DTE user selectable (on/off)
CTS, DCD, DSR	Normal or Force On
Connector	Winchester (V.35), female

MENU TREE

Menu	1) Status	5) Port Status	1) DTE Data/Clock	
			2) DTE Status	
			3) DTE Port Rate	1) DS0 Rate
			4) DBU Data/Control	2) TX CLK Control
			5) DBU Control	3) Data
			6) DBU Status	4) CTS
				5) DCD
				6) DSR
			1) Nx56/64 Config	7) 0 Inhib
				8) Inband Mode
	2) Config	7) Port Config	1) Backup Mode	
			2) Backup On	
			3) Pattern Verify	
		2) DBU Config	4) Backup Delay	
			5) Restore Delay	
			6) Retry Delay	
			7) Num Retries	1) Backup Test
			8) Backup Testing	2) Test Hour
			9) Wk End Lockout	3) Test Day
			A) Enable HR	
			B) Disable HR	
		2) Factory Restore		
	3) Util	5) Port Utility	SW Rev	
			1) DTE Loopback	
			2) 511 Pattern	
			3) 511 Results	
		2) Run Self-test	4) DBU Loopback	
			5) DBU Test	
	4) Test	3) Port Test	6) DBU Data/ Cntr	
			7) DBU Test Result	

V.35 WINCHESTER CONNECTION PINOUT

Pin	CCITT	Description
A	101	Protective ground (PG)
B	102	Signal ground (SG)
C	105	Request to send (RTS) from DTE
D	106	Clear to send (CTS) to DTE
E	107	Data set ready (DSR) to DTE
F	109	Received line signal detector (DCD) to DTE
R	104	Received data (RD-A)
T	104	Received data (RD-B)
V	115	RX clock (RC-A) to DTE
X	115	RX clock (RC-B) to DTE
P	103	Transmitted data (TD-A) from DTE
S	103	Transmitted data (TD-B) from DTE
Y	114	TX clock (TC-B) to DTE
AA	114	TX clock (TC-B) to DTE
U	113	External TX Clock (TC-A) from DTE
W	113	External TX clock (ETC-B) from DTE
NN	-	Test mode (TM) to DTE